

AMENDMENTS TO THE CLAIMS

The following is a complete, marked-up listing of revised claims with a status identifier in parenthesis, underlined text indicating insertions, and strike through and/or double-bracketed text indicating deletions.

LISTING OF CLAIMS

Claims 1.-14. (Canceled)

15. (Currently Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a current output terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current output terminal of the driving transistor via a wire or a transistor, the second terminal being a terminal opposite to the first terminal; and

a third switching transistor for connecting the second terminal of the second capacitor to a voltage line, wherein the second terminal of the second capacitor is connected to a node between the second and third switching transistors, the voltage line being set to a value which corresponds to an anode potential from a property of the current driving light emitting element.

16. (Currently Amended) The display apparatus as set forth in claim 15, wherein:

during a first period within a current writing period of the driving transistor, the first switching transistor connects the current control terminal to the current output terminal, the second switching transistor disconnects the second terminal and the current output terminal from each other, and the third switching transistor connects the second terminal to the voltage line, ~~the second switching transistor operating in an opposite logic state from the third switching transistor;~~

during a second period within the current writing period, the first switching transistor disconnects the current control terminal from the current output terminal, and the third switching transistor disconnects the second terminal from the voltage line, and the second switching transistor connects the second terminal to the current output terminal, the second switching transistor operating in an opposite logic state from the third switching transistor in the first and second periods; and

during a readout period of the driving transistor, the second switching transistor disconnects the second terminal from the current output terminal, and the driving transistor supplies a current to the current light emitting element.

17. (Previously Presented) The display apparatus as set forth in claim 16, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit of a plurality of source driver circuits.

18. (Previously Presented) The display apparatus as set forth in claim 16, wherein:

one or more of the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in a pixel circuit, and the others are provided in a portion outside the pixel circuit, which portion includes a source driver circuit.

19. (Previously Presented) The display apparatus as set forth in claim 18, wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit; and

the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in the portion outside the pixel circuit, which portion includes the source driver circuit,

said display apparatus, further comprising:

a connecting wire for connecting the current control terminal of the driving transistor to the first terminal of the second capacitor.

20. (Previously Presented) The display apparatus as set forth in claim 19, wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit;

the second capacitor, the first switching transistor are provided outside the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver circuit;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to the second switching transistor and the third switching transistor.

21. (Previously Presented) The display apparatus as set forth in claim 18, wherein:

the current driving light emitting element, the driving transistor, the first switching transistor, the first capacitor, and the second capacitor are provided in the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver circuit or the portion outside the pixel circuit;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to (i) the current output terminal of the driving transistor, or (ii) the current input terminal of the driving transistor.

22. (Previously Presented) The display apparatus as set forth in claim 15, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit of a plurality of source driver circuits.

23. (Previously Presented) The display apparatus as set forth in claim 22, wherein:

each of the source driver circuits includes the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor; and

each of the pixel circuits includes a transistor for controlling a current that is to be supplied to the current driving light emitting element.

24. (Currently Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a current input terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current input terminal of the driving transistor via a wire and a transistor, the second terminal being a terminal opposite to the first terminal; and

a third switching transistor for connecting the second terminal of the second capacitor to a voltage line, wherein the second terminal of the second capacitor is connected to a node between the second and third switching transistors, the voltage line being set to a value which corresponds to an anode potential from a property of the current driving light emitting element.

25. (Currently Amended) The display apparatus as set forth in claim 24, wherein:

during a first period within a current writing period of the driving transistor, the first switching transistor connects the current control terminal to the current input terminal, the second switching transistor disconnects the second terminal and the

current output terminal from each other, and the third switching transistor connects the second terminal to the voltage line, ~~the second switching transistor operating in an opposite logic state from the third switching transistor;~~

during a second period within the current writing period, the first switching transistor disconnects the current control terminal from the current input terminal, and the third switching transistor disconnects the second terminal from the voltage line, and the second switching transistor connects the second terminal to the current input terminal, the second switching transistor operating in an opposite logic state from the third switching transistor in the first and second periods; and

during a readout period of the driving transistor, the second switching transistor disconnects the second terminal from the current input terminal, and the driving transistor supplies a current to the current light emitting element.

26. (Previously Presented) The display apparatus as set forth in claim 25, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit of a plurality of source driver circuits.

27. (Previously Presented) The display apparatus as set forth in claim 25, wherein:

one or more of the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in a pixel circuit, and the others are provided in a portion outside the pixel circuit, which portion includes a source driver circuit.

28. (Previously Presented) The display apparatus as set forth in claim 27,
wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit; and

the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in the portion outside the pixel circuit, which portion includes the source driver circuit,

said display apparatus, further comprising:

a connecting wire for connecting the current control terminal of the driving transistor to the first terminal of the second capacitor.

29. (Previously Presented) The display apparatus as set forth in claim 28,
wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit;

the second capacitor, the first switching transistor are provided outside the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to the second switching transistor and the third switching transistor.

30. (Previously Presented) The display apparatus as set forth in claim 27,
wherein:

the current driving light emitting element, the driving transistor, the first switching transistor, the first capacitor, and the second capacitor are provided in the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver circuit or the portion outside the pixel circuit;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to (i) the current output terminal of the driving transistor, or (ii) the current input terminal of the driving transistor.

31. (Previously Presented) The display apparatus as set forth in claim 24, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit of a plurality of source driver circuits.

32. (Previously Presented) The display apparatus as set forth in claim 31, wherein:

each of the source driver circuits includes the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor; and

each of the pixel circuits includes a transistor for controlling a current that is to be supplied to the current driving light emitting element.

33. (Currently Amended) A method for driving a display apparatus including a current driving light emitting element and a driving transistor, the method comprising the steps of:

electrically connecting a current control terminal of the driving transistor to a first terminal of a first capacitor;

electrically connecting, during a current writing period of the driving transistor, the first terminal of the first capacitor to a first terminal of a second capacitor;

during a first period, (i) electrically connecting a second terminal of the second capacitor to a voltage line by a third switching transistor, and (ii) electrically connecting the current control terminal of the driving transistor to a current output terminal of the driving transistor by a second switching transistor, and (iii) causing the first capacitor and the second capacitor to retain a current control terminal potential that the driving transistor has on this occasion, the second terminal being a terminal opposite to the first terminal, wherein the second terminal of the second capacitor is connected to a node between the second and third switching transistors, the voltage line being set to a value which corresponds to an anode potential from a property of the current driving light emitting element;

during a second period, (i) correcting the current control terminal potential by disconnecting the current control terminal of the driving transistor from the current output terminal of the driving transistor by a first switching transistor, and by changing electric connection of the second terminal of the second capacitor from the voltage line to the current output terminal of the driving transistor by the second and third transistor, and (ii) causing the first capacitor to retain the current control terminal potential that the driving transistor has on this occasion; and

controlling, during a current readout period of the driving transistor, an output current of the driving transistor with the use of the current control terminal potential, retained by the first capacitor, of the driving transistor.

34. (Previously Presented) The driving method as set forth in claim 33, wherein:

during the second period, the electric connecting of the second terminal of the second capacitor to the current output terminal of the driving transistor is carried out before disconnecting the voltage line from the second terminal of the second capacitor.